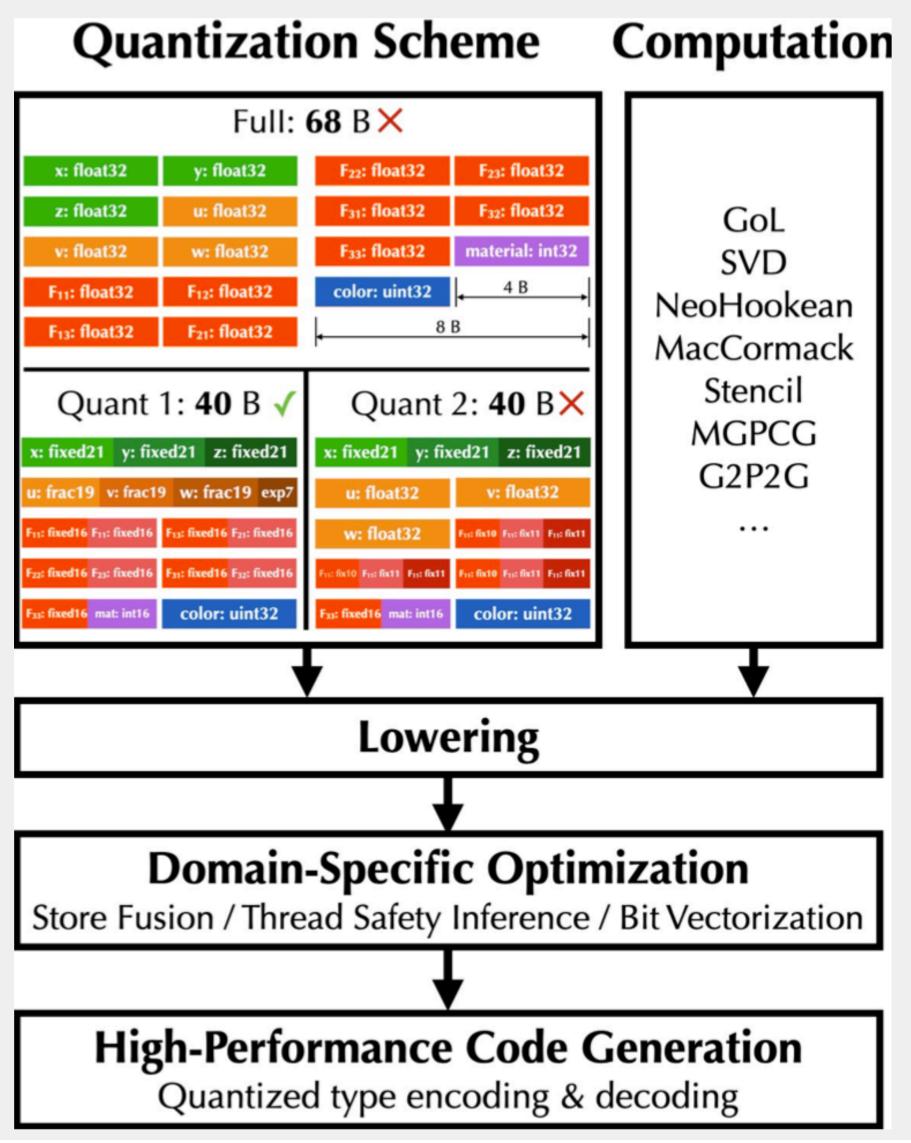




Introduction

- High-resolution simulations can deliver great visual quality, but they are often limited by available memory, especially on GPUs. • We present a compiler for physical simulation that can achieve both high performance and significantly reduced memory costs, by enabling flexible and aggressive quantization.



Key ideas

- Use bit-level compression to save memory!
- •An example: packing a 2D coordinate (`x` and `y`) into 32 bits.

			y	
	0.995	54	0.01	(
x (IEEE 754 "float")	sign _x exp _x 00111111011)1
y (IEEE 754 "float")	sign _y exp _y		,)1
, (,	0011110000			
x, y: ti.quant.float (exp =6, fraction =13)	exp _{xy} sign _x 011110 <mark>0</mark> 1111	frac _x sig		
x, y: ti.quant.float	exp _{xy} sign _x	frac _x sig 111011016 Ic _x exp _y	000000010 , sign _y frac	91 Cy
		sign _x exp _x x (IEEE 754 "float") 00111111011 sign _y exp _y	x (IEEE 754 "float") 0011111101111101101 sign _y exp _y fr	sign _x exp _x frac _x x (IEEE 754 "float") 00111111011111011000000 sign _y exp _y frac _y

QuanTaichi: A Compiler for Quantized Simulations

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Methods

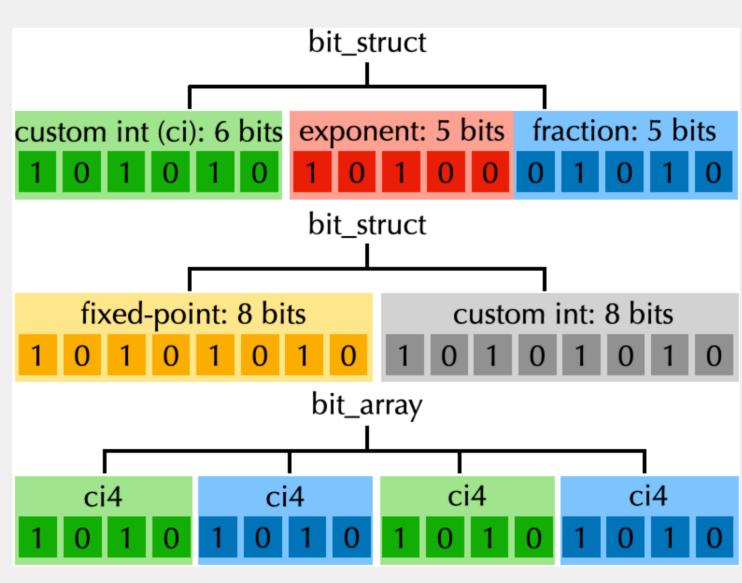
To realize quantized simulations, we need to implement new type systems.

• Custom numerical data types:

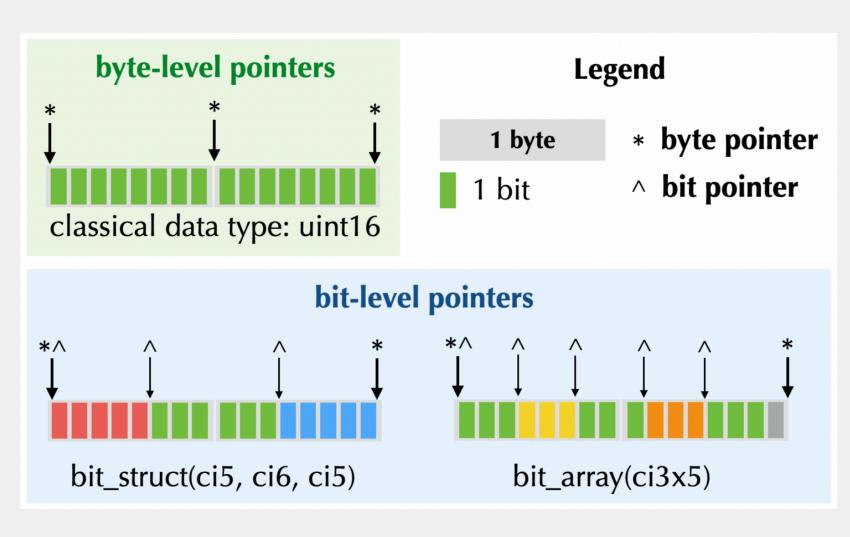
We support 3 types of custom data types: custom integer, fixedpoint numbers and floating-point numbers

> # custom integer i5 = ti.quant.int(bits=5) # fixed-point real numer fixed17 = ti.quant.fixed(frac=17, range=3.14) # floating-point real numer f18 = ti.quant.float(exp=4, frac=14)

- Bit adapters
- •We use bit adapters to pack custom data types into hardwarenative types
- Bit structs: structs of different custom types.
- •Bit arrays: arrays of repeated custom types.



- Bit levels pointers
- Bit level pointers are `traditional byte pointers + bit offset`.
- •With bit pointers we can locate each single bit.



- Domain specific optimizations
- Some of the operations involved in quantized simulations could be quite costly, so we propose the following domain specific optimizations for performance:
- Bit struct store fusion
- Thread safety inference
- Bit array vectorization

- 00
- 1111
- 1000
- 1001
- 0101
- 0101

Results

•With our compiler we implement the following three, to our knowledge, largest scale simulations on a single GPU respectively.

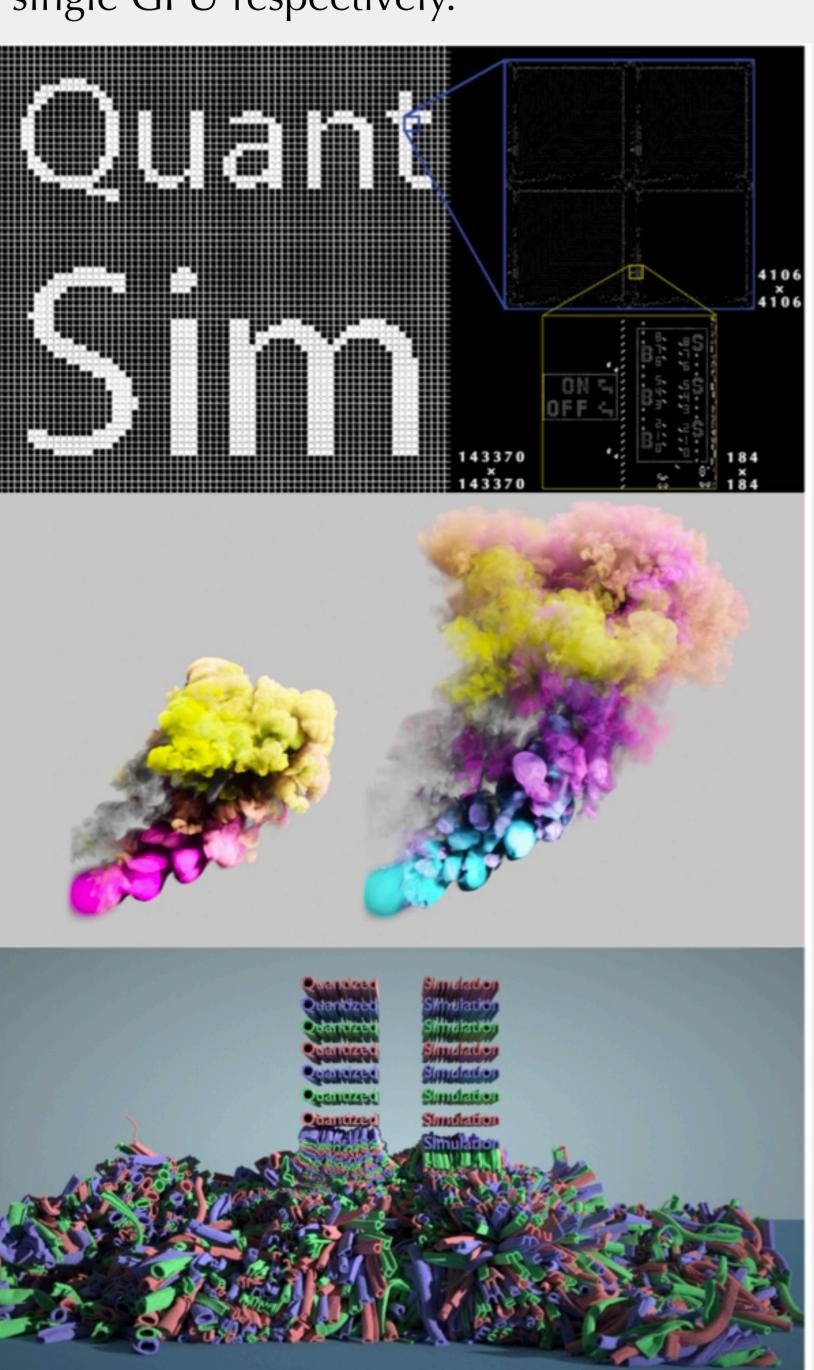
Game of Life 7.0 GB memory 20,554,956,900 cells Per cell: 2 B \rightarrow 0.25 B (8.0x)

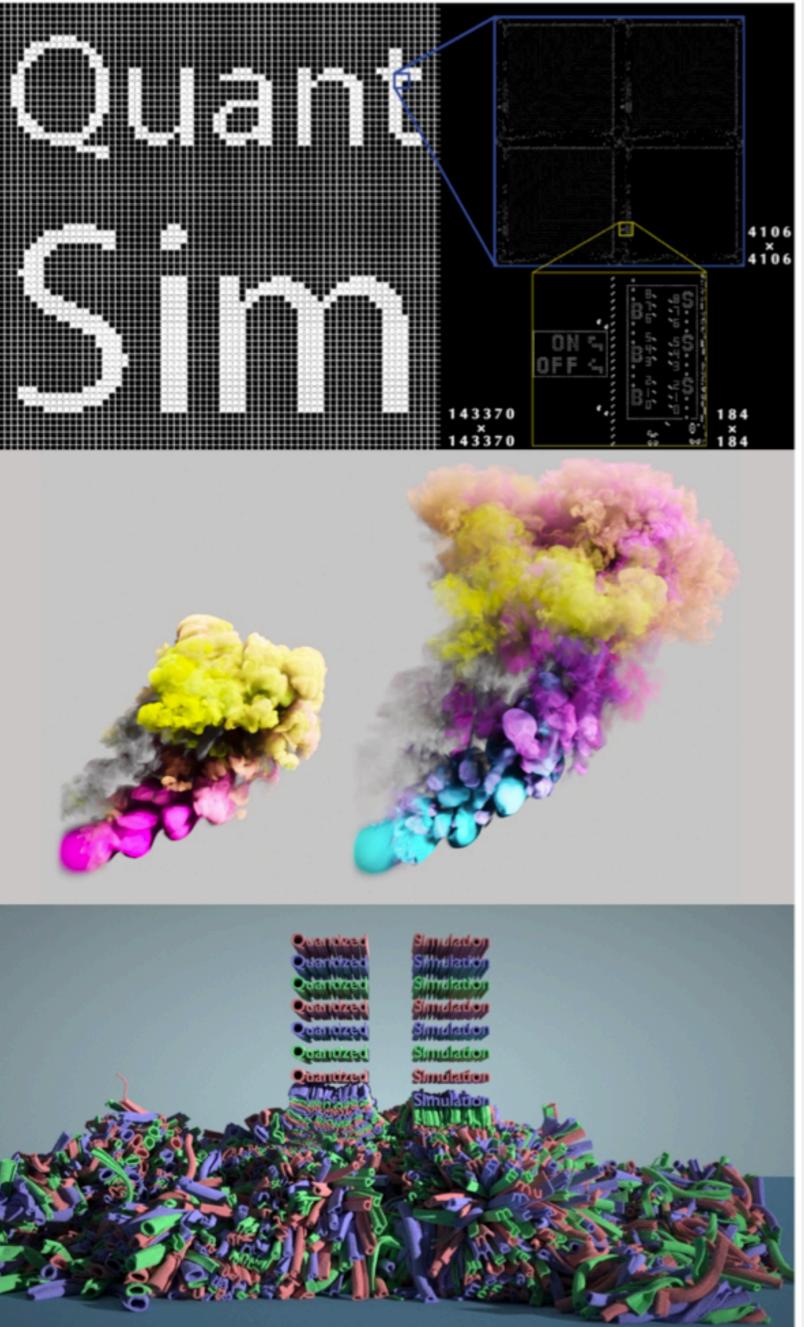
Advection-Reflection

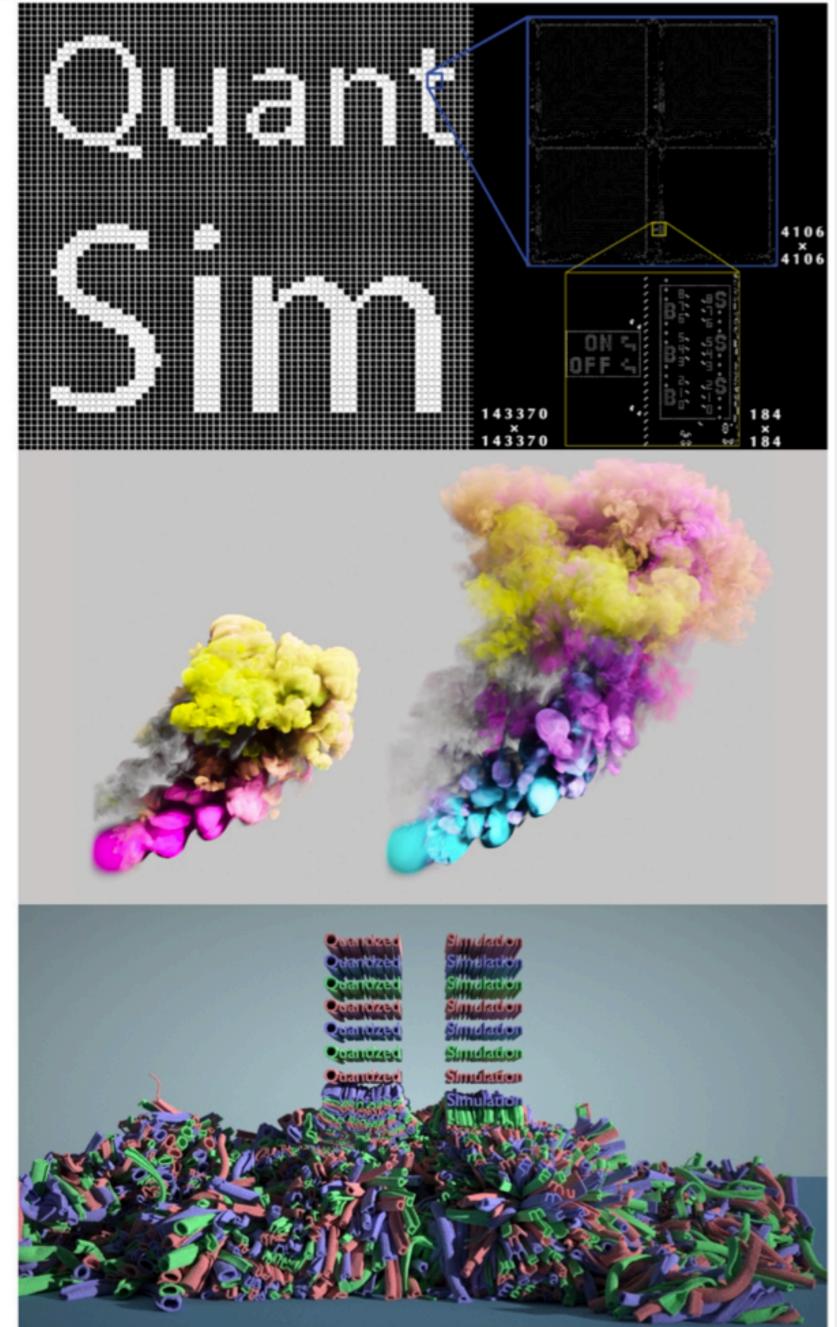
29.3 GB memory 421,134,336 voxels Per voxel: 110 B \rightarrow 70 B (1.6x)

MLS-MPM

16.6 GB memory 234,527,481 particles Per particle: 68B→40B (1.7x)







- Performance
- Thanks to the domain specific optimizations and memory bandwidth even better the non-quantized simulators in our experiments.
- Please check our paper for more details.

Conclusions

- with our compiler.
- Easy to use. No more than 3% LoC modification can make a traditional simulator quantized.
- The performance is comparable, sometimes even better than fullprecision version.
- No significant visual quality degradation (more details in paper)

saving, the performance of the quantized simulators is comparable and

•Memory saving: users can save memory(1.6x ~ 8x in our experiments)